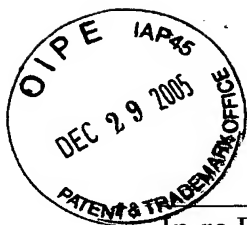


JPW



Docket No.: M4065.0960/P960
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Alexander Krymski

Application No.: 10/705,843

Confirmation Number: 4875

Filed: November 13, 2003

Art Unit: 2878

For: PIXEL SIGNAL BINNING AND
INTERPOLATION IN COLUMN CIRCUITS
OF A SENSOR CIRCUIT

Examiner: T. X. Luu

SUBMISSION OF DRAWINGS

MS PGPUB Drawings
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Submitted herewith is one set (eleven sheets, eighteen figures) of drawings for filing in the above-identified patent application. Kindly substitute the enclosed drawings for the drawings submitted with the originally filed application.

Dated: December 29, 2005

Respectfully submitted,

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